

Page 37, line 3, delete "is held" and insert -- holds --;  
line 18, delete "more" and insert --further --.

IN THE CLAIMS:

Please amend the claims as follows:

*Sub D47.7*  
*CS* (twice amended) A semiconductor integrated circuit comprising:

a chip;

first circuits provided on said chip;

second circuits provided on said chip; and

voltage limiter means provided on said chip for reducing an external supply voltage to an internal supply voltage lower than said external supply voltage within said chip;

wherein, when said external supply voltage is not higher than a predetermined first voltage, the [output] internal supply voltage of said voltage limiter means increases at a first rate which is substantially equal to the increasing rate of said external supply voltage, when said external supply voltage is between a level exceeding said first voltage and a predetermined second voltage, said [output] internal supply voltage increases at a second rate which is lower than the increasing rate of said external supply voltage, and after said external supply voltage exceeds said second voltage, said internal supply voltage increases at a third rate which is higher than the [increasing rate thereof when said external supply voltage is between a level exceeding said first voltage and said second voltage] second

CS rate, wherein said first circuits are fed said internal supply voltage, said first circuits are fed a control signal and wherein a driving ability of said voltage limiter means is controlled by said control signal.

Claim 9, line 2, insert -- of -- after "made inside".

Claim 11, line 2, insert -- third -- after "wherein the";  
line 5, delete "the" and insert -- a fourth --.

Sub D5 13.7 (second amendment) A semiconductor integrated circuit comprising:  
CS  
a chip;  
load circuits provided on said chip; and  
[a] voltage limiter means provided on said chip for reducing an external supply voltage to an internal supply voltage lower than said external supply voltage within said chip and supplying it to said load circuits;

wherein said load circuits are fed said internal supply voltage, said load circuits are fed a control signal and wherein a driving ability of said voltage limiter means is controlled by said control signal [the internal supply voltage is output from said voltage limiter means in response to the operation of said load circuits by controlling a signal for controlling said voltage limiter means and a signal for controlling said load circuits].

Claim 15, line 1, delete "14" and insert -- 13 --;  
line 2, insert -- of -- after "made inside".

16. (twice amended) A semiconductor integrated circuit according to claim 15, wherein, when said external supply voltage is not higher than a predetermined first voltage, the [output] internal supply voltage of said <sup>internal power supply</sup> voltage limiter means increases at a first rate which is substantially equal to the increasing rate of said external supply voltage, when said external supply voltage is between a level exceeding said first voltage and a predetermined second voltage, said [output] internal supply voltage increases at a second rate which is lower than the increasing rate of said external supply voltage, and after said external supply voltage exceeds said second voltage, said internal supply voltage increases at a third rate which is higher than the [increasing] second rate [thereof when said external supply voltage is between a level exceeding said first voltage and said second voltage].

Claim 18, line 2, insert -- third -- after "wherein the";  
line 5, delete "the" and insert -- a fourth --.

Please cancel claims 8 and 14 without prejudice or disclaimer of the matter therein.

#### REMARKS

Claims 7-19 stand rejected under 35 USC 112, second paragraph as being indefinite for failing to particularly point